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P440.12-0006Application No.:
10/616,801First Named Inventor:
Kurt Kimber et al.Filing Date:
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Group Art:

2936

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT

U.S. PATENT DOCUMENTS

Examiner Initials	Document No.	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents
AA	6,429,489	08-06-2002	Botula et al. 257/355
AB	6,549,061	04-15-2003	Voldman et al. 327/483
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AM				
AN				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	"An On-Chip ESD Protection Circuit with Low Trigger Voltage in BiCMOS Technology", Albert Z. H. Wang and Chen-Hui Tsay, <i>IEEE Journal of Solid-State Circuits</i> , Vol. 36, No. 1, January 2001.
AP	
AQ	

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.